# Nanostructured Electrodes for Improved Neural Recording

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### ABSTRACT

We present a new method of increasing the effective electrode surface for improved neural recording. To optimize the electrode, the impedance can be decreased by introducing surface roughness or nanostructures on the electrode. High aspect ratio pillar-like polysilicon nanostructures are created in a reactive ion etch. Nanostructure robustness in cell culture is examined.

## INTRODUCTION

The interface between neurons and electrodes is one of the key issues in implantable microdevices and bioelectronics. The ideal electrode requires maximum selectivity and minimum impedance. During *in vivo* experiments, electrode insertion inevitably results in tissue damage, and signals from living cells must travel through the attenuating layer of damaged cells. Selectivity refers to the ability to select a single neuron from a multitude of interconnected cells or even damaged cells. High impedance in an electrode attenuates and filters the measured signal. In addition, a sufficient signal-to-noise ratio is required for data analysis; low electrode impedance gives high signal gain [1]. However, an increase in electrode selectivity with smaller geometric electrode size results in increased impedance and noise. To optimize the electrode, the impedance per unit of geometric surface area must be decreased by altering its nanostructure. With the introduction of surface roughness or nanostructures, a square  $20~\mu m \times 20~\mu m$  electrode has only 400 square microns of geometric (planar) area but a much larger effective area.

Effective electrode surface area can be increased through micropatterning, surface roughening, and chemical modification [2]. In micropatterning, structures such as grooves or holes are fabricated on the electrodes. If this pattern is etched into the substrate, the metal that forms the electrode must then be deposited conformally. Although an aspect ratio as high as 100:1 can be achieved using deep reactive ion etching, the metal deposition method (e.g. evaporation or sputtering) ultimately limits the aspect ratio and thus the final effective surface area of the micropatterned structures. These structures are typically on the micro scale. Another method of fabricating such structures is by first photolithographically defining the structures on top of the electrode and then electroplating metal using photoresist as a mold.

Electrode materials historically used in neuroscience are gold, platinum, and iridium. The most common surface modifications are wet chemical etching for gold, electroplating platinum black or ion milling the platinum surface, or activating iridium to form iridium oxide. However, they suffer from some drawbacks such as harsh chemical treatment in the case of gold etching, and poor adhesion of platinum powder to the electrode [3, 4].

The junction between the cell and the electrode can be described by equivalent circuits which have the following elements: the space charge layer, charge transfer resistance, Warburg impedances, spreading resistance, shunt resistance, and other parasitic capacitances. Because the total electrode impedance is approximately proportional to the effective surface area, a 10 times increase in surface area should give an order of magnitude decrease in impedance.

We present a new method of increasing the effective electrode surface for improved neural recording. The introduction of nanostructures on the electrode pads will significantly increase the effective electrode surface area and decrease impedance. High aspect ratio pillar-like polysilicon nanostructures are created in a reactive ion etch. Nanostructure robustness in cell culture is examined.

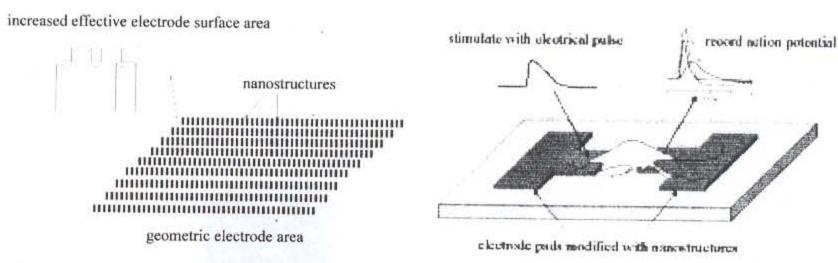


Fig. 1(a) Nanostructures on an electrode increase the effective surface area.

Fig. 1(b) Schematic of experimental setup for nanostructured electrodes.

## MATERIALS AND METHODS

## Nanostructure Fabrication:

The substrate was a 1  $\mu$ m film of heavily n-type in-situ doped polysilicon, which was deposited by low-pressure chemical vapor deposition (LPCVD) onto an oxide-coated silicon wafer. It was annealed at 900 °C for 30 minutes to activate the dopants in the polysilicon.

- Electrode pads were defined by conventional I-line lithography and a two-step anisotropic plasma etch (Lam Research 9400 TCP): a breakthrough step removes the native oxide and an etch step patterns the polysilicon.
- After stripping the photoresist, nano-pillars are formed on the electrodes, also by using a two-step anisotropic plasma etch.

The Lam Research 9400 TCP etcher is a single wafer, plasma reactive ion etch (RIE) system that has top and bottom powered parallel electrode plates. CF<sub>4</sub> is used during the initiation phase of the etch (breakthrough step) to remove the native oxide. Cl<sub>2</sub> is the main etch gas for polysilicon. HBr is used to improve the selectivity between polysilicon and oxide; O<sub>2</sub> is used in conjunction with HBr because it passivates the polysilicon during etching.

Table 1. Etch Parameters

Structure:	Breakthrough Step	Etch step	Polysilicon Etch rate
1. Electrodes	100 sccm CF <sub>4</sub> 13 mTorr pressure	50 sccm Cl <sub>2</sub> + 150 sccm HBr 15 mTorr pressure	550 nm/min
	200 W, top	300 W, top	20:1 selectivity
	40 W, bottom	150 W, bottom	(polysilicon:oxide)
2. Nano-pillars	(short 10 sec. etch) 100 secm CF <sub>4</sub>	200 sccm HBr + 5 sccm O <sub>2</sub> 35 mTorr pressure	480 nm/min
	13 mTorr pressure	250 W, top	200:1 selectivity
	200 W, top 40 W, bottom	120 W, bottom	(polysilicon:oxide)

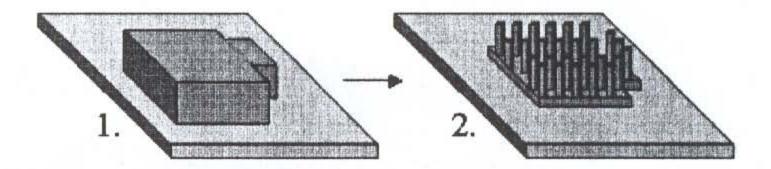


Fig. 2. Nanostructure fabrication:

- 1. A polysilicon film is deposited on an oxide surface and patterned into the electrode shape;
- The film is etched back in a highly selective reactive ion etch, which produces the nanostructures.

## Neuron Dissociation and Culture:

Embryos were rapidly removed on embryonic day 18 following cervical dislocation of a pregnant rat. The embryos were decapitated, and the brains were carefully removed and placed in warm HEPES-buffered saline solution. The hippocampi were removed and treated with trypsin for 15 min at 37 °C, followed by washing and gentle trituration. The dissociated cells were plated at densities of 20,000 – 50,000 cells/ml on poly-L-lysine treated nanostructure-patterned substrates in 35 mm Petri dishes. The plating medium was DMEM supplemented with 10% heat-inactivated fetal bovine serum, 10% Ham's F12 with glutamine and 50 U/ml penicillin–streptomycin. Twenty-four hours after plating, the culture medium was changed to the above medium containing 20 mM KCl. Medium was partially exchanged 3 times each week. Cultures prepared in this manner provide neurons that can survive *in vitro* for 8 weeks.

## Biological Sample Preparation for SEM:

The cultured samples were fixed in 2% glutaraldehyde in 0.1 M sodium cacodylate buffer, pH 7.2. After a post-fix in 1% osmium tetroxide in 0.1 M sodium cacodylate buffer and rinsing,

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the samples were dehydrated in ethanol and dried in the critical point dryer. The samples were mounted onto stubs using carbon tape and coated with 5 nm carbon by evaporation.

#### RESULTS AND DISCUSSIONS

The nanostructures were densely packed as shown in Fig. 3(a). The pillars were  $0.3~\mu m$  tall, and their diameters ranged from 20 nm to 100 nm. This gives a greater than 20 times increase in the surface area of the substrate. Fig. 3(b) shows the nanostructures on top of each electrode pad increase the effective electrode surface area and hopefully decrease impedance during cell recording.

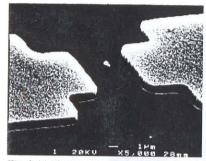


Fig. 3 (a) The nanostructures on top of each electrode pad.

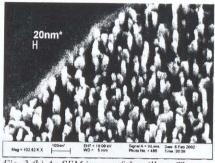


Fig. 3 (b) An SEM image of the pillars. The pillars are 20 nm in diameter.

A change in the etch parameters explains the different etch selectivity (compare the electrode and nano-pillar etch steps in Table 1) and why both microscale electrode pads and nanoscale pillars can be made in the same etcher. During the nano-pillar etch step, native oxide and dust on the surface of the polysilicon serve as micromasks. This mask initiates the formation of nano-pillars in the highly directional etch. As the nano-pillars are etched down further, they are passivated with a thin layer of silicon oxybromide because  $O_2$  acts as oxidant to generate  $Si_xO_yBr_z$ . This continuous passivation prevents the HBr from etching the silicon underneath, resulting in taller and taller nano-pillars with continued etching. The density and diameter of the nano-pillars are controlled by the ratio of HBr and  $O_2$ .

After two weeks in culture, it was obvious that our preparations were infected with bacteria; only a few hippocampus neurons remained in the culture. SEMs show that the bacteria strongly prefer to attach to the nanostructure-patterned areas.

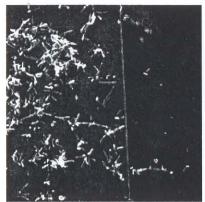
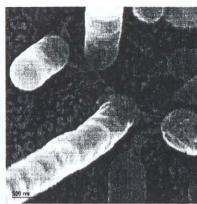


Fig. 4(a) The rod-shaped bacteria preferentially attach on the nano-pillars (left) and not on the unpatterned surface (right).



The rod-shaped bacteria Fig. 4(b) Close-up view of bacteria attach on the nano-pillars attachment structures on the nano-pillars.

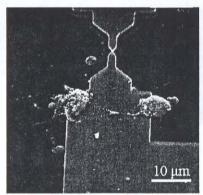


Fig. 5(a) Two neurons on an electrode pad.

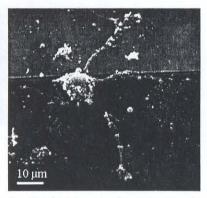


Fig. 5(b) A neuron extending a process onto a nano-pillar region (top).

Neuronal response to electrical stimulation will be recorded from electrodes that have nanostructures and compared to recordings from plain electrodes to study electrode impedance as shown in Fig. 1(b).

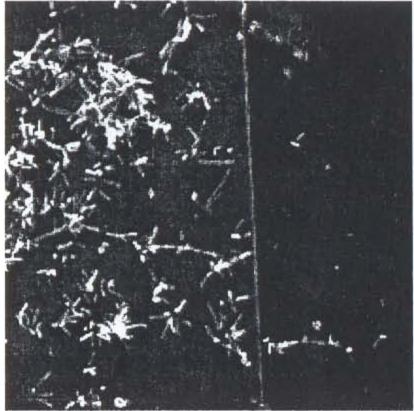


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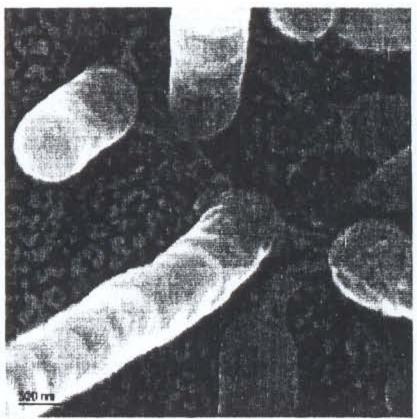


Fig. 4(b) Close-up view of bacteria attachment structures on the nano-pillars.

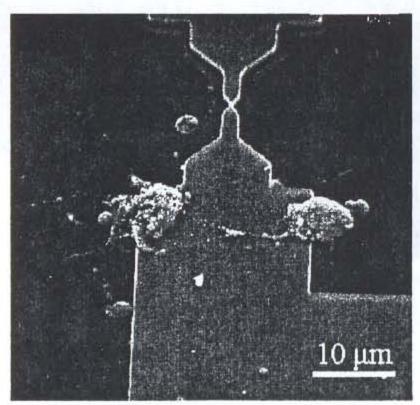


Fig. 5(a) Two neurons on an electrode pad.

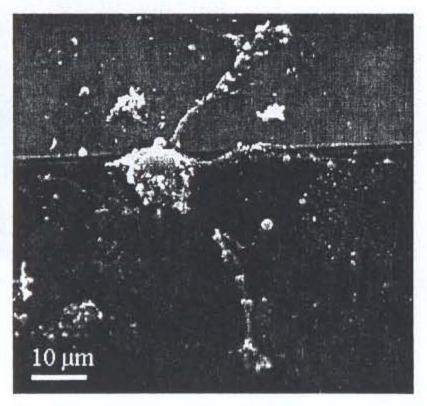


Fig. 5(b) A neuron extending a process onto a nano-pillar region (top).

Neuronal response to electrical stimulation will be recorded from electrodes that have nanostructures and compared to recordings from plain electrodes to study electrode impedance as shown in Fig. 1(b).

## CONCLUSIONS

High aspect ratio pillar-like polysilicon nanostructures were fabricated. Their diameters ranged from 20 to 100 nm; diameter and packing density are controlled by the ratio of HBr and O<sub>2</sub> in a highly selective silicon RIE. Bacteria preferentially attach to the nano-pillar patterned areas.

In future experiments, neuronal response to electrical stimulation will be recorded from electrodes that have nanostructures and compared to recordings from plain electrodes to study electrode impedance. Applications of these nano-pillars extend beyond electrodes, because these nanostructures may improve integration of tissue with an implanted micro device as cells anchor onto the increased surface area that they provide.

## ACKNOWLEDGMENTS

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### REFERENCES

- [1] A. Blau, Ch. Ziegler, M. Heyer, F. Endres, G. Schwutzgebel, T. Matties, T. Stieglitz, J. U. Meyer, and W. Goepel, "Characterization and optimization of microelectrode arrays for in vivo nerve signal recording and stimulation," iosensors & Bioelectronics, 12(9-10):883:892, 1997.
- [2] D. A. Stenger and T. M. McKenna, editors. Enabling Technologies for Cultured Neural Networks. Academic Press, New York, 1994.
- [3] P. Thiebaud. Fabrication of microelectrode arrays for electrophysiological monitoring of hippocampal organotypic slice cultures by interface. PhD thesis, Institute of Microtechnology, University of Neuchatel, Switzerland, 1999.
- [4] J. D. Weiland and D. J. Anderson, "Chronic Neural Stimulation with Thin-Film, Iridium Oxide Electrodes," IEEE Transactions on Biomedical Engineering, 47(7): 911-918, July 2000.