

# A Comprehensive Modeling of Dynamic Negative-Bias Temperature Instability in PMOS Body-Tied FinFETs

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**Abstract**—This paper presents a novel approach to estimate the rising and falling behavior of  $N$ th-order ON-state current by dynamic negative-bias temperature instability (DNBTI), with a comparison between experimental data and a modified DNBTI model in PMOS body-tied FinFETs for the first time. The modified model was proposed to predict not only  $N$ th-order DNBTI behavior but also temperature and stress bias effects. The fin-width dependence was analyzed, and different trends between silicon-on-insulator and body-tied FinFETs were explained with the extracted DNBTI model parameters: stress time, oxide-field strength, and temperature. The proposed model closely matched the measured static lifetime.

**Index Terms**—Body tied, double gate, dynamic negative-bias temperature instability (DNBTI), FinFET, floating-body, reliability, silicon-on-insulator (SOI).

## I. INTRODUCTION

MULTIGATE FinFET structures are promising nanoscale devices having features of high robustness on short-channel effects and excellent scalability using conventional processes [1], [2]. As the device is scaled down, the negative-bias temperature instability (NBTI) starts to limit the device reliability of digital and analog CMOS circuits [3], [4]. Previous studies indicate an improvement to the negative-bias-temperature (NBT) stress with a wide fin width in silicon-on-insulator (SOI) and body-tied FinFETs [5], [6]. Recently, a recovery of the NBTI has become a concern for the ac-lifetime prediction of the device [7], [8]. In this paper, dynamic-NBTI (DNBTI) reliabilities of fully depleted body-tied PMOS FinFETs were investigated and modeled through a new approach with consideration of stress biases ( $V_{st}$ ), fin widths ( $W_{Fin}$ ), body temperatures ( $T$ ), and  $-0.2$  V substrate biases ( $V_{sub}$ ).

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## II. EXPERIMENTS

Negative biases ( $V_G = V_{TO} - V_{st}$ ,  $V_{TO} = V_G$  at  $I_D = -100$  nA and  $V_D = -50$  mV) for stress states and positive biases ( $V_G = V_{TO} + V_{st}$ ) for recovery states were applied to body-tied FinFET gates. Additionally, the source/drain and the substrate were grounded with various values of  $T$ : 50 °C, 80 °C, 125 °C, and 150 °C. A  $V_{sub}$  of  $-0.2$  V was applied to certify the virtual-floating-body effects of SOI FinFETs, which was similar to a previous study [4]. Detailed fabrication processes have already been reported [2]. To interpret the failure mechanisms of DNBTI, a change of the ON current ( $I_{ON} = I_D$  at  $V_G - V_{TO} = -1$  V and  $V_D = -1$  V) after the stresses and the electrochemical-reaction models at the Si/SiO<sub>2</sub> interface [9], [10] were used.

## III. RESULTS AND DISCUSSIONS

Fig. 1 shows the DNBTI degradation and enhancement with various values of  $V_{st}$ ,  $T$ ,  $W_{Fin}$ , and  $V_{sub}$ . An  $I_{ON}$  degradation on the stress state represents an increment of the interface state ( $N_{it}$ ) and the oxide-trap charge ( $N_{ot}$ ) by Si-H bond breaking; furthermore, its enhancement on the recovery state represents  $N_{ot}$  neutralization and  $N_{it}$  repassivation. A  $V_{st}$  increment induced more holes at the interface, broke more Si-H bonds in stress states, and promoted more rediffused hydrogenated species for  $N_{it}$  and more accumulated electrons for  $N_{ot}$  in recovery states [7], [11]. An increment of  $T$  weakened the Si-H bonds in stress states and expedited rediffusion of repassivation species in recovery states. A  $W_{Fin}$  reduction induced more holes in stress states and more passivated  $N_{ot}$  due to accumulated electrons in recovery states. An increment of the hole concentration by accumulated electrons at the center of the fin due to virtually floating-body ( $V_{sub} = -0.2$  V) is dominant in the low-oxide-field ( $E_{ox}$ ) condition [5]. In contrast, a decrease of hole concentration from the substrate to the gate is dominant in high- $E_{ox}$  condition due to negative  $V_{sub}$  in stress states [12]. More electrons were induced at the interface due to forwardly biased substrate in recovery states. For an analytical and comprehensive understanding of DNBTI with  $V_{st}$ ,  $T$ ,  $W_{Fin}$ , and  $V_{sub}$ , the previous model [10] was revamped with the introduction of a fitting parameter  $\kappa$  to describe the cyclic  $I_{ON}$  variation in Fig. 1 as follows:

$$\Delta I_{ON} = A\kappa t^n (E_{ox})^m \exp\left(-\frac{E_a}{kBT}\right) \quad (1)$$

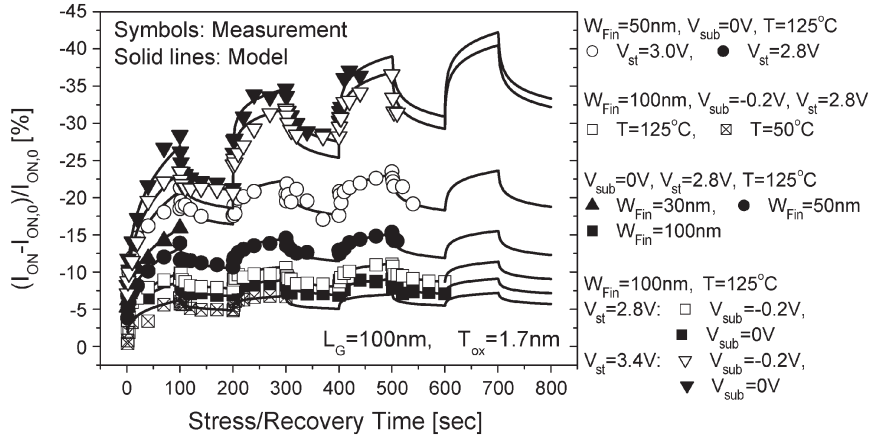


Fig. 1.  $\Delta I_{ON}/I_{ON,0}$  of DNBTI versus stress/recovery time with various  $V_{st}$ ,  $T$ ,  $W_{Fin}$ , and  $V_{sub}$ . Solid lines represent the modeled DNBTI profiles using the proposed method, which was an attractive approach to predict the  $N$ th-order stress/recovery profiles.

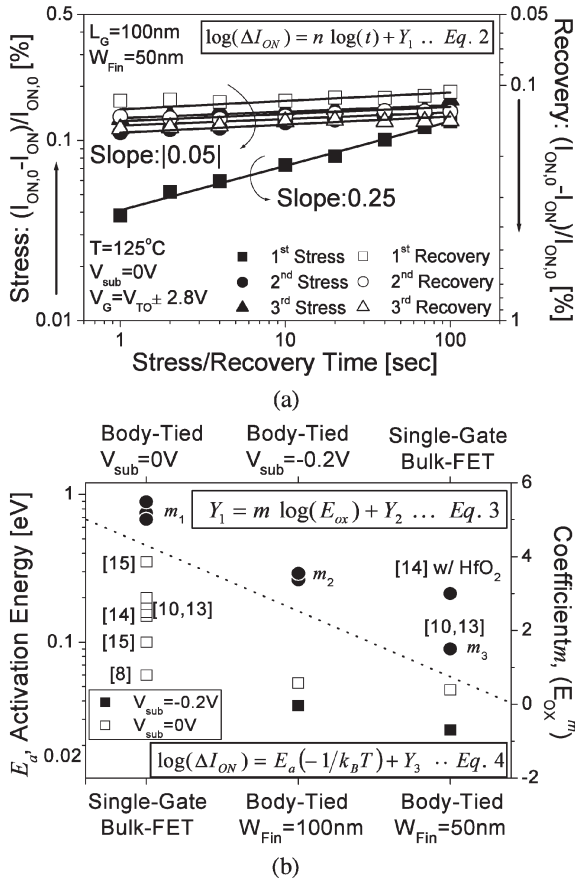


Fig. 2. (a)  $I_{ON}$  degradation and enhancement with a number of stress and recovery states. First stress  $n$  is fixed to 0.25 and reduced to  $\pm 0.05$  after the first recovery and the second stress. (b) Right and top axis shows  $m$  with a single-gate and body-tied FinFET ( $V_{sub} = 0$  and  $-0.2$  V). Left and bottom axis shows  $E_a$  of body-tied FinFETs with  $W_{Fin}$  values of 50 nm and 100 nm and a single-gate bulk FET (infinite  $W_{Fin}$ ).

where  $A$  is a proportional factor to tailor the order of magnitude of  $I_{ON}$ ;  $t$  is the stress time, i.e., aging time; and  $E_a$  is the activation energy of the holes to break Si-H bonds.

Fig. 2(a) shows  $I_{ON}$  variation on stress and recovery states versus stress/recovery time.  $n$  is the exponent for the time dependence and extracted from Eq. 2 in the inset of Fig. 2(a). After the first stress,  $n$  was 0.25 and was then reduced to

TABLE I  
EXTRACTED COEFFICIENTS  $A$ ,  $n$ ,  $m$ , AND  $E_a$  AFTER THE FIRST STRESS WITH  $W_{Fin} = 50$  nm, 100 nm AND  $V_{sub} = 0$  V,  $-0.2$  V.  $E_a$  SHOWS  $W_{Fin}$  DEPENDENCE, AND  $A$ ,  $m$ , AND  $E_a$  SHOW  $V_{sub}$  DEPENDENCE

$W_{Fin}$	$V_{sub}$	$A$	$n$	$m$	$E_a$
100nm	0V	$2 \times 10^{-37}$	0.25 $\pm 0.05$	5	0.053eV
	-0.2V	$1 \times 10^{-26}$	0.25 $\pm 0.05$	3.5	0.037eV
50nm	0V	$2 \times 10^{-37}$	0.25 $\pm 0.05$	5	0.048eV
	-0.2V	$1 \times 10^{-26}$	0.25 $\pm 0.05$	3.5	0.025eV
Bulk FET	-	-	0.25	1.5	0.06eV~ 0.35eV

$\pm 0.05$  after the first recovery and the second stress. The + sign corresponds to stress states and the - sign corresponds to recovery states. The coefficient  $n = 0.25$  comes from the diffusion mechanism [9], [10], and the reduction of  $n$  ( $= \pm 0.05$ ) comes from the lock-in mechanism [7]. This reduction was caused by insufficiently passivated  $N_{it}$  or  $N_{ot}$  due to the lost hydrogenated species during the first stress. The exponent  $n$  was independent of  $W_{Fin}$ ,  $V_{st}$ ,  $T$ , and  $V_{sub}$ . Fig. 2(b) shows the extracted  $m$  from Eq. 3 in the inset of Fig. 2(b), the exponent for the  $E_{ox}$ , such as  $m_1$  ( $V_{sub} = 0$  V, FinFET);  $m_2$  ( $V_{sub} = -0.2$  V, FinFET); and  $m_3$  (single gate).  $m$  did not show  $W_{Fin}$  dependence, but it showed  $V_{sub}$  dependence.  $m$  increased as the number of gates increased, i.e.,  $m_{1,2} > m_3$  [10], [14]. In the inset of Fig. 2(b), Eq. 4 was used to extract  $E_a$ , which was larger in the single-gate bulk planar MOSFET than the body-tied FinFETs [8], [10], [13]–[15].  $E_a$  decreased as  $W_{Fin}$  decreased and a negative  $V_{sub}$  was applied. Table I summarizes the extracted coefficients:  $A$ ,  $n$ ,  $m$ , and  $E_a$  after the first stress. The virtual floating-body,  $V_{sub} = -0.2$  V, indicated the decrement of  $m$  and  $E_a$ . Thus, NBTI was worse at an SOI than at a bulk substrate, which was consistent with the previous report [5].

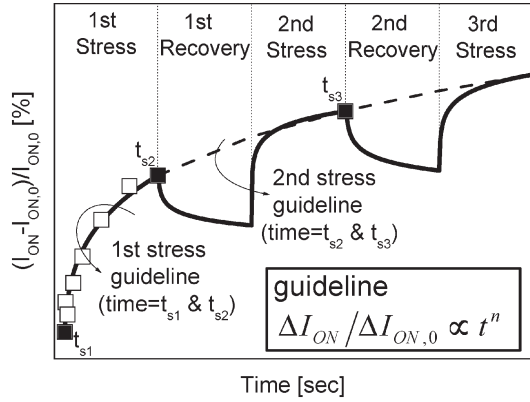


Fig. 3. Periodic  $\Delta I_{ON}/I_{ON,0}$  under DNBT stress. The first stress guideline increased according to  $t^n$  with  $n = 0.25$  and changed to  $n = 0.05$  at the second guideline.

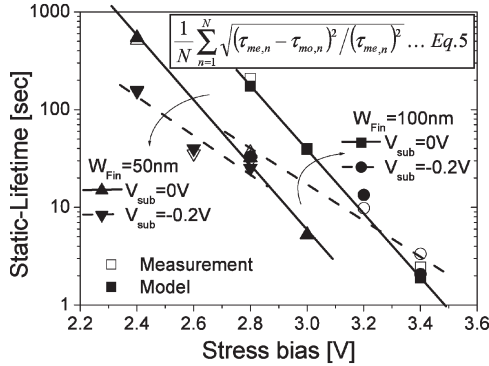


Fig. 4. Measured and modeled static lifetimes at  $W_{Fin} = 50$  nm, 100 nm and  $V_{sub} = 0$  V,  $-0.2$  V versus  $V_{st}$  according to (1) and the parameters in Table I. The root-mean-square error is 16%.

Fig. 3 shows an estimation of the  $I_{ON}$  variation using the measured data under the DNBT stress. Guidelines of  $\Delta I_{ON}/I_{ON,0}$  under the NBT stress increased according to the power law  $t^n$ , while the guideline after the first stress was extracted from  $t_{s1}$  to  $t_{s2}$  with  $n = 0.25$ . Additionally, the  $I_{ON}$  variation between  $t_{s2}$  and  $t_{s3}$  was predicted a second stress guideline with  $n = 0.05$  [16]. The solid lines of Fig. 1 represent the modeled DNBTI profiles using the proposed method, which was an attractive approach to predict the  $N$ th-order stress or the recovery with  $V_{st}$  and  $T$ . Periodic stress and recovery profiles are well explained using (1), the parameters in Table I, the first and second guidelines, and the fitting parameter  $\kappa$ . Fig. 4 shows the static lifetime predicted by the model versus  $V_{st}$ . A measured lifetime ( $\tau_{me}$ ) and a modeled lifetime ( $\tau_{mo}$ ) were compared, and the root-mean-square error using Eq. 5 in the inset of Fig. 4 was 16%.

#### IV. CONCLUSION

A modified DNBTI model and an extraction method were developed to predict the  $N$ th-order DNBTI profile with various  $V_{st}$ ,  $T$ ,  $W_{Fin}$ , and  $V_{sub}$ . The stress-time exponent  $n$  was

0.25 at the first stress state and was changed to  $\pm 0.05$  after the first recovery state. A decrement of  $E_a$  with a narrower  $W_{Fin}$  represented the increment in  $N_{it}$  and device degradation. A virtual floating-body indicated a decrement of the coefficient  $m$  and  $E_a$ . The modeled static lifetime coincided well with the measured static lifetime showing root-mean-square error of 16%.

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