

## Editorial

# Micro/Nanostructured Arrays: Fabrication, Applications, and Devices

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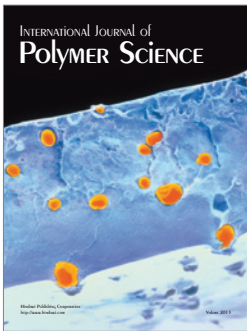
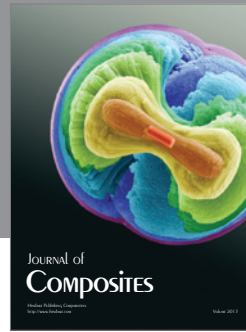
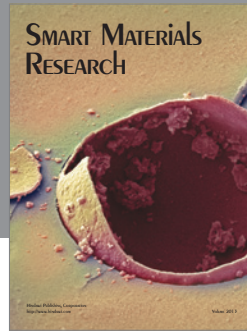
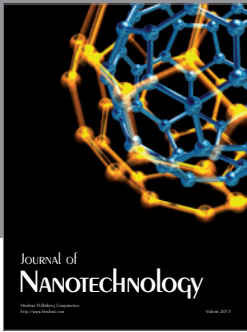
Recently, ordered micro/nanostructured arrays have attracted much interest due to their important applications in field emitters, nanogenerators, sensors, microfluidic devices, optoelectronic devices, and so forth. The traditional methods to fabricate micro/nanostructured arrays are generally based on lithographic techniques, for example, photolithography, electron beam lithography, ion beam lithography, and X-ray lithography, to name a few. However, these technologies are usually high costs and time consumption.

Therefore, scientists also endeavored to find other alternative strategies by facile routes with low costs. In this issue, S. Oh et al. and W.-W. Zhong et al. reported the self-assembly growth of one-dimensional oxide semiconductor nanostructures like TiO<sub>2</sub> nanotube and ZnO nanorods. The influence of visible light irradiation and annealing on the photoluminescence and drug elution activity properties was investigated. G. Liu et al. reported the fabrication of self-standing silver nanoplate arrays by seed-decorated electrochemical route using colloidal crystal as template. M. Veis and R. Antos review the advances in optical and magneto-optical scatterometry of periodically ordered nanostructured arrays. J. Hu et al. reported the metal surface plasmon resonance in periodic hexagonal lattice arrays of silver nanodisks, while E. J. Lee et al. reported the effect of liquid gallium on metal surfaces and characterized the morphology and chemical composition of metals heated in liquid gallium. As for device applications, Y. Takahashi et al. reported the

multifunctional logic gate by means of nanodot array with different arrangements, and H. J. Kim et al. reported small-sized flat-tip CNT emitters for miniaturized X-ray tubes.

We believe that this specially organized issue could help readers to get more information on the fabrication of micro/nanostructured arrays and their device applications.

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